

PATENT NUMBER

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APPLICATION NO. 09/987978	CONT/PRIOR D F	CLASS 257	SUBCLASS C00	ART UNIT 1734 3715	EXAMINER J. Clark
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Leadframe semiconductor integrated circuit device using the same, and method of and process for fabricating the two

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<input type="checkbox"/> <b>Form of the patent</b> <input type="checkbox"/> <b>has been</b> <u>disclaimed</u> (date) _____ <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drawn <u>31</u> Figs. Drawn <u>36</u> Print Figs. <u>3</u>		<b>CLAIMS ALLOWED</b> Total Claims <u>13</u> Print Claim for O.G. <u>1</u>	
	_____ (Assistant Examiner) (Date) _____		<b>NOTICE OF ALLOWANCE MAILED</b>  	
<b>FINAL SPRE REVIEW</b> _____ (INITIALS)	<u>JASMIN CLARK</u> (Primary Examiner)		<b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
	_____ (Legal Instruments Examiner) (Date) _____		<b>ISSUE BATCH NUMBER</b>  	
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